

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] The present application claims the priority of Japanese Patent Application No. 2001-012600, filed in Japan on January 22, 2001, the entire contents of which are hereby incorporated herein by reference.

BACKGROUND OF THE INVENTION

1. Technical field of the invention:

[0002] The invention relates to a method of cleaning a chemical vapor deposition system(referred to in the present patent specification as "CVD system").

2. Description of Related Art:

[0003] It is known to use high-temperature polysilicon-type TFTs (thin-film transistors) and low-temperature polysilicon-type TFTs in methods of manufacturing liquid crystal displays.

[0004] In order to obtain high-quality oxide films in the manufacturing methods using high-temperature polysilicon-type TFTs, quartz substrates which could withstand high temperatures of 1000°C or more are used.

[0005] In contrast, in the manufacture of low-temperature polysilicon-type TFTs, it is necessary to carry out film deposition in a low-temperature environment (for example 450° C or less) because a glass substrate which is customary for TFTs is used. Methods for manufacturing liquid crystal displays using low-temperature polysilicon-type TFTs have the advantage that they do not require special substrates to be used. Such methods have been put into practice in recent years and their production volume is continuing to expand.

[0006] In the manufacture of liquid crystal displays using low-temperature polysilicon-type TFTs, plasma CVD is used when a silicon oxide film is deposited as a gate insulator film at low temperature. When silicon oxide film is deposited

by plasma CVD, silane, tetraethoxysilane (TEOS) and the like are used as typical materials in gas form.

[0007] If silane, or the like, is used as the material in gas form and silicon oxide film is deposited by means of plasma, in the conventional plasma CVD system, silicon oxide film is deposited on the surface of a substrate by introducing the material in gas form and oxygen, or the like, into the space in front of said substrate, generating plasma in a gas mixture comprising the material in gas form and the oxygen or the like and exposing the substrate to said plasma.

[0008] The conventional plasma CVD systems are configured in such a way that the material in gas form is supplied directly into the plasma which is generated inside the plasma CVD system. For this reason, with the configuration of conventional plasma CVD system, there is a problem that the high-energy ions are injected from the plasma present in the space in front of the substrate onto the film-depositing face of the substrate and they damage the silicon oxide film and degrade the properties of the film. Furthermore, as the material in gas form is fed directly into the plasma, particles are produced by violent reaction between the material in gas form and the plasma, and as a result the yield is reduced.

[0009] In order to solve the above mentioned problems, an attempt to improve the CVD device of the remote plasma type is disclosed in Japan Patent Application Serial Number H11-157692.

[0010] The CVD device disclosed in the above mentioned the patent application, Serial Number H11-157692, produces active seeds (radicals) by generating plasma inside a vacuum vessel, carries out the film-deposition processing on a substrate, accommodated inside said vacuum vessel, by means of these active seeds and material in gas form.

[0011] That is to say, an electrically conductive partition plate which divides the interior of said vacuum vessel into two chambers is provided in said vacuum vessel. The interior of one of these two chambers is formed as a plasma-generating space in which high-frequency electrode are arranged, and the interior

of the other chamber is formed as a film-deposition processing space in which a substrate-holding mechanism on which a substrate is mounted is arranged. A plurality of through-holes which are made to pass from the plasma-generating space to the film-deposition processing space are formed in this electrically conductive partition plate. Furthermore, this electrically conductive partition plate has an interior space which is divided off from the plasma-generating space and communicates with the film-deposition processing space via a plurality of diffusion holes. The system is configured in such a way that the material in gas form is supplied to the interior space of this electrically conductive partition plate from the outside and fed into said film-deposition processing space through said plurality of diffusion holes. The active seeds which are generated in said plasma-generating space are fed into the film-deposition processing space through the plurality of through-holes formed in said electrically conductive partition plate and film processing is performed on said substrate in film-deposition processing space.

[0012] In said CVD system disclosed in Patent Application Serial Number H11-157692, the plurality of through-holes which are made to pass from said plasma-generating space and are provided in said electrically conductive partition plate to said film-deposition processing space are formed to satisfy the condition $uL/D > 1$ when the gas flow velocity inside said through-holes is u , the effective length of the through-holes is L and the coefficient of mutual gas diffusion is D .

[0013] As the plasma-generating space and film-deposition processing space are separated by means of the electrically conductive partition plate in said CVD system proposed in Patent Application Serial Number H11-157692, the device is configured in such a way that the processing surface of the substrate which is arranged in the film-deposition processing space is not exposed to the plasma. In addition, a plurality of through-holes which are made to pass from the plasma-generating space to film-deposition processing space are formed in the electrically conductive partition plate. However, because these through-holes are formed so as to satisfy the above-mentioned condition, the material in gas form which is fed

into the film-deposition processing space is prevented from diffusing back into the plasma-generating space.

[0014] It is to be noted that in Patent Application Serial Number H11-157692, a CVD system is proposed which is formed in such a way that said plurality of diffusion holes also fulfill the above mentioned condition placed on the through-holes, in order to prevent the active species fed into the film-deposition processing space from diffusing back into the interior space of the partition plate.

[0015] In fact, Patent Application Serial Number H11-157692 discloses a CVD system in which plasma is generated between the high-frequency electrode and the lower face part of the upper part of the vacuum vessel and in the space which is bounded by the high-frequency electrode and the partition wall comprised of vacuum vessel which makes up the CVD system and the electrically conductive partition plate, both of which are at ground potential. Further more, the variation of the above mentioned CVD system is disclosed in which, the high frequency electrodes are installed in upper positions in the plasma-generating space and plasma electrical discharge is produced between the high-frequency electrode and the electrically conductive partition plate.

[0016] Generally, there are problems common to CVD systems that when films continue to be deposited, they are also deposited on the substrate-supporting elements and the interior wall of the film-depositing chamber and the like. When they drop off onto the substrate during film deposition as particles, they cause to be disconnect circuits of the wiring and result in the reduction of the yield of manufactures products.

[0017] For this reason, apart from the film-depositing process, optimum cleaning is carried out after processing the prescribed number of substrates, said cleaning being performed using particular cleaning gases according to differences in the plasma-forming method and structures and compositions of the deposited materials. The cleaning of this type of CVD device is an important process, as is the film-deposition process in the implementation of stabilized operation of the

CVD system without exposing the interior of the depositing chamber to the atmospheric ambient.

OBJECTS AND SUMMARY

[0018] An object of the present invention is to provide an optimum cleaning process for the CVD system disclosed in Patent Application Serial Number H11-157692.

[0019] In the manufacture of large liquid crystal displays in which low temperature polysilicon-type TFTs are used, the CVD device disclosed in Patent Application Serial Number H11-157692 uses plasma and deposits silicon oxide film on a large-area substrate using material in gas form, such as silane, in order to form at low temperatures a suitable silicon oxide film as a gate insulator film. An appropriate cleaning method is proposed which is suitable for this disclosed CVD system and a method for cleaning the CVD system is proposed in which the generation of particles is sufficiently suppressed, high manufacturing-product yield by means of said CVD system is maintained and said CVD system can carry out stable operations without exposing the interior of the depositing chamber to the atmospheric ambient.

[0020] A method of cleaning a CVD device according the present invention can be used in the CVD system disclosed in Patent Application Serial Number H11-157692. According to one aspect of the present invention with an electrically conductive partition plate placed at ground potential, cleaning gas is fed into a plasma-generating space, active species are generated by applying high-frequency electric power to the high-frequency electrodes arranged in said plasma-generating space, said generated active species are fed into a film-deposition processing space through a plurality of through-holes in said electrically conductive partition plate and said film-deposition processing space is cleaned by means of said active species fed into this film-deposition processing space.

[0021] That is to say, in the CVD system which is disclosed in Patent Application Serial Number H11-157692, the plasma-generating space and the film-deposition processing space are separated from one another by an electrically conductive partition plate and a plurality of through-holes is made to pass from the plasma-generating space to the film-deposition processing space in said electrically conductive partition plate. The through-holes are formed such that they fulfil conditions which prevent back-diffusion to the plasma-generating space side of the material in gas form fed from the film-deposition processing space.

[0022] Cleaning gas is fed directly into the plasma-generating space, which is separated from the film-deposition processing space by the electrically conductive partition plate, and active species (radicals) are generated by applying high-frequency electric power to the high-frequency electrode inside said plasma-generating space. The generated active species (radicals) are fed into the film-deposition processing space through the plurality of through-holes in the electrically conductive partition plate, which is at ground potential, and the film-deposition processing space is cleaned by means of the active species fed into the film-deposition processing space.

[0023] According to the present invention, it is possible to use fluoride gas as the cleaning gas. One or more types of the fluoride gases from such as, for example, NF₃, F₂, SF₆, CF₄, C₂F₆, C₃F₈ can be used.

[0024] When fluoride gas is used as the cleaning gas to apply the present invention to the actual cleaning, after processing of a prescribed number of silicon oxide films and a-Si films, fluoride gas is fed into the plasma-generating space, and active species (fluorine radicals) are generated by striking electrical discharge in the plasma-generating space. The fluorine radicals are fed into the film-deposition processing space through the plurality of through-holes in the electrically conductive partition plate at ground potential, and the film-deposition processing space is cleaned. In other words, deposits attached to the inner walls of the vacuum vessel and to the surface of the substrate-holding mechanism, and

the like, react with said fluorine radicals and thus can be removed and expelled from an exhaust port.

[0025] In this respect, oxygen gas can be added to the above mentioned fluoride gas in order to further the dissociation into the fluorine atom radicals. For example, J. Appl. Phys. Vol. 52 (1981) p.162 proposes that by adding oxygen with a concentration of 60% or less, it is possible to increase the density of fluorine atom radicals in comparison with cases in which there is no additive.

[0026] If fluoride gas is used, as mentioned above, the radicals generated inside the plasma-generating space are fluoride radicals or fluorine atom radicals. However, but in cases where the deposits on the film-processing space and the like are carbonates, O₂ is used as the cleaning gas.

[0027] In addition, in cases in which the density of the plasma is low and a sufficient cleaning speed is not obtained, if an inert gas with a high ionization potential such as He, Ne, Ar, Kr and Xe is admixed with the cleaning gas, it is possible to raise the temperature of the electrons by the admixture of said inert gas, to further the dissociation of the cleaning gas such as fluoride gas and to increase the cleaning speed.

[0028] In cases in which the method of cleaning a CVD system according to the present invention is implemented using fluoride gas as the cleaning gas as mentioned above, the cleaning gas which is adsorbed in the inner face of the through-holes, in the partition plate and on the partition plate during the cleaning step may desorb in the progress of the film-depositing step after the completion of the cleaning, may be discharged into the film-deposition processing space from the interior of the partition plate, and fluorine which is produced due to cleaning gas may be included in the thin film during the film deposition after the completion of the cleaning and degrades the intrinsic properties of the thin film.

[0029] The present application proposes a method of cleaning a CVD system which, as mentioned above, can suppress in advance the above mentioned problem

which occurs in cases in which fluoride gas is used as the cleaning gas in the cleaning method according to the present invention.

[0030] In the method of cleaning the above-mentioned CVD system, according to one aspect of the present invention, when, with the electrically conductive partition plate at ground potential, cleaning gas is fed into said plasma-generating space, active species are generated by applying high-frequency electric power to the high-frequency electrode arranged in the interior of said plasma-generating space and said generated active species are fed into said film-deposition processing space through the plurality of through-holes in said electrically conductive partition plate. The electrically conductive partition plate is heated, and more specifically, heating of said electrically conductive partition plate can be carried out within a temperature range which suppresses the adsorption of fluorine onto the inner circumferential face of said through-holes and the surface of the partition plate.

[0031] The temperature range at which the adsorption of fluorine onto the inner circumferential face of the through-holes and the surface of the partition plate is prevented varies respectively depending on the type of fluoride gas used as cleaning gas. For example, in cases in which the cleaning gas is a fluorocarbon gas such as CF_4 , C_2F_6 , C_3F_8 and in cases when the cleaning gas is a nitrogen fluoride gas such as NF_3 the electrically conductive partition plate is heated to 200°C or more, and in cases when the cleaning gas is a fluorosulfur gas such as SF_6 the electrically conductive partition plate is heated to 100°C or more.

[0032] Such heating of the electrically conductive partition plate can be carried out, for example, by housing heating means, such as a heater, in the electrically conductive partition plate.

[0033] With the respective cleaning method, as it is possible to heat the electrically conductive partition plate to the necessary temperature at which the adsorption of cleaning gas onto the inner circumferential face of said plurality of through-holes provided in said electrically conductive partition plate and the

surface of the partition plate is inhibited, said heating being in accordance with the type of fluoride gas used as cleaning gas, it is possible to remove the fluorine which is absorbed onto the inner circumferential face of the through-holes and the surface of the partition plate during the cleaning and to prevent in advance the fluorine contamination of the thin film during the film deposition of the film-depositing process after the completion of cleaning.

BRIEF DESCRIPTION OF THE FIGURES

[0034] Figure 1 is a view of a vertical section showing the configuration of a first embodiment of a CVD system in which the present invention can be applied.

[0035] Figure 2 is a view of a vertical section showing the configuration of a second embodiment in which the present invention can be applied.

[0036] Figure 3(a) is an enlarged sectional view of places where the partition plate is fixed.

[0037] Figure 3(b) is an enlarged sectional view of an embodiment of the partition plate in which heating means are housed.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0038] An embodiment of a CVD system in which a cleaning method according to the present invention can be applied will be described with reference to figures 1 and 2.

[0039] The CVD device shown in figures 1 and 2 are preferably used when silane is employed as the material in gas form, and silicon oxide film is formed as the gate insulator film on an upper surface of a glass substrate 11 which is customary for a TFT.

[0040] When film-deposition processing is carried out in the vacuum vessel 12, the vessel interior is maintained in a desired vacuum state by means of an exhaust device 13. The exhaust device 13 is connected to an exhaust port 12b-1 formed in the vacuum vessel 12.

[0041] In the interior of the vacuum vessel 12, a partition plate 14, which is made of electrically conductive material, is installed in a horizontal state. The partition plate 14, which has a planar, for example, rectangular, shape, is arranged in such a way that its peripheral part forms a sealed state by pushing it down and attaching it to the lower face of the electrically conductive material fixing part 22. In this way, the interior of the vacuum vessel 12 is separated into two chambers in the upward and downward directions by the partition plate 14. The upper chamber is made into the plasma-generating space 15, and the lower chamber is made into the film-deposition processing space 16.

[0042] The partition plate 14 has a desired specific thickness and is entirely in the shape of a flat plate. Furthermore, it is of planar shape similar to the shape of the horizontal section of the vacuum vessel 12. An interior space 24 is formed in the partition plate 14, and a plurality of through-holes 25 which fulfill specific conditions are formed and distributed throughout the interior space 24. The plasma-generating space 15 and the film-deposition processing space 16 communicate only via the through-holes 25.

[0043] A glass substrate 11 is arranged on a substrate-holding mechanism 17 installed in the film-deposition processing space 16. The glass substrate 11 is essentially parallel with the partition plate 14 and is arranged in such a way that its film-depositing face (upper face) is facing the lower face of the partition plate 14. The potential of the substrate-holding mechanism 17 is kept at ground potential 41 which is the same potential as the vacuum vessel 12. Furthermore, a heater 18 is installed in the substrate-holding mechanism 17. This heater 18 is used to keep the temperature of the glass substrate 11 at a prescribed temperature.

[0044] The vacuum vessel 12 is configured, from the point of view of improving its assembly properties, of an upper vessel 12a which forms the plasma-generating space 15 and a lower vessel 12b which forms the film-deposition processing space 16. When the vacuum vessel 12 is formed by assembling the upper vessel 12a and lower vessel 12b, the electrically conductive

partition plate 14 is installed between the two. In order to ensure that it is placed at ground potential, the partition plate 14 is mounted so as to make contact with the electrically conductive material fixing part 22, in the manner as shown in figure 3(a), for example. In this way, the separated plasma-generating space 15 and film-deposition processing space 16 are formed on the upper and lower sides of the partition plate 14 and the plasma-generating space 15 is formed by means of the partition plate 14 and upper vessel 12a.

[0045] In the embodiment shown in figure 1, the region in which plasma 19 is generated in the plasma-generating space 15 is formed from the partition plate 14, upper vessel 12a and plate-shaped electrode (high-frequency electrode) 20 which are arranged in an approximately central position.

[0046] A plurality of holes 20a are formed in the electrodes 20. The electrodes 20 are supported and fixed by means of two insulator parts 21a, 21b which are installed along the inner face of the side part of the upper vessel 12a.

[0047] An is to be noted that the electrically conductive element 32 is sandwiched between the partition plate 14 and the fixing part 22, which is made of electrically conductive material and which is positioned inside the vacuum vessel 12. The partition plate is fixed to the the fixing part 22 by means of a mounting screw 33 (figure 3(a)). The electrically conductive element 32 is a cord-shaped electrically conductive element which has spring properties in the manner of what is referred to as a spiral shield, and it ensures electric contact between the partition plate 14 and the fixing part at ground potential and absolutely noleakage to the film-processing space of high frequency waves. However, provided that the partition plate 14 is mounted in such a way that it is reliably kept at ground potential when the CVD device is being cleaned, it is not restricted to the structure in figure 3(a).

[0048] In addition, the heater 30 for heating the interior partition plate 14 can be accommodated in the partition plate 14 as shown in figure 3(b). In this case, a feed pipe 28 is arranged above the heater 30.

[0049] In cases in which fluoride gas is used as the cleaning gas, the heater 30 is placed in such a way that the partition plate 14 is heated up to at least the necessary temperature, at which adsorption of the cleaning gas into the inner circumferential face of the plurality of through-holes 25 provided in the partition plate 14 and the surface of the partition plate is prevented, said heating being carried out in accordance with the type or types of fluoride gas. In this respect, the number of heaters 30 accommodated, and the state in which they are arranged, can be freely determined in accordance with the size of the partition plate 14 and the necessary parameters of the heating temperature, and the like. In addition, it is possible to accommodate in the partition plate 14, in the same way as the heater 30, a thermocouple detection sensor (not shown in the figure) or the like for detecting the heating temperature of the partition plate 14.

[0050] Provided that the structure and heating means and the like for heating the partition plate 14 are configured so as to fulfill the above objective, they are not restricted to the form shown in figure 3(b).

[0051] Feed pipes 23a, 23b, which introduce oxygen gas and cleaning gas into the plasma-generating space 15 from the outside, are installed in an insulator part 21a. The oxygen gas feed pipe 23a and cleaning gas feed pipe 23b are connected to an oxygen gas supply and cleaning gas supply (neither shown in the figure) through a mass flow controllers which controls the flow.

[0052] A fluoride gas such as NF_3 , F_2 , SF_6 , CF_4 , C_2F_6 , C_3F_8 can be used as the cleaning gas.

[0053] In cases in which the etch rate of the cleaning gas when etching silicon oxide film is low, it is possible to add to the cleaning gas an inert gas such as He, Ne, Ar, Kr or Xe which is intended to increase the radical density by further

raising the dissociation rate of the cleaning gas as a result of the rise of electron temperature of the plasma.

[0054] As the method introducing these additive gases, it is possible to continuously introduce these additive gases from oxygen gas feed pipe 23a, or from the gas feed pipe midway connected to the cleaning gas pipe 23b, or newly installed independent feed gas pipe exclusively provided for the additive gases.

[0055] In the interior of the vacuum vessel 12, the plasma-generating space 15 is separated from the film-deposition processing space 16 by the partition plate 14. However, a plurality of through-holes 25, which fulfil specific conditions, are formed in the partition plate 14 so as to penetrate the interior space 24. The plasma-generating space 15 and the film-deposition processing space 16 communicate only via the through-holes 25.

[0056] Furthermore, a plurality of diffusion holes 26 which supply material in gas form to the film-processing space 16 are formed in the lower wall of the partition plate 14.

[0057] In order to prevent the material in gas form fed into the film-deposition processing space 16 from diffusing back to the plasma-generating space 15 side, the above-mentioned through-holes 25 are formed so as to fulfill the condition $uL/D > 1$ where the gas flow velocity inside the through-holes 25 is u , the effective length of these through-holes 25 is L and the coefficient of mutual gas diffusion (the coefficient of mutual gas diffusion of two types of gas on the two sides of the through-holes 25) is D . If said conditions which are applied to through-holes 25 is applied, to the diffusion-holes, it more effectively prevents the active species from diffusing back to the interior space 24 of the partition plate 14.

[0058] A feed pipe 28 for introducing material in gas form is connected to the interior space 24. The feed pipe 28 is arranged so as to be connected from the outside. In addition, in order to ensure that the material in gas form is supplied uniformly from the diffusion holes 26, a homogenizing plate 27, which has a

plurality of holes perforated therein, is installed approximately horizontally in the middle of the interior space 24.

[0059] An electric power feed rod 29, which is connected to the electrodes 20, is installed in a ceiling part of the upper vessel 12a. High-frequency electric power to be discharged to the electrodes 20 is supplied by the electric power feed rod 29. The electrodes 20 can function as high-frequency electrode.

[0060] The electric power feed rod 29 is covered by an insulator 31, and insulation from other metal surface can be.

[0061] In the CVD system according to the embodiment shown in figure 2 the structure of the electrodes is modified in comparison with the embodiment shown in figure 1, and the high-frequency electrode 20 are installed in a position on the upper side of the plasma-generating space 15, plasma electric discharges being generated between the high-frequency electrodes 20 and the partition plate 14.

[0062] The basic structural elements are essentially the same as the structural elements of the CVD system according to the embodiment presented in figure 1 and identical reference symbols have been used for common structural elements, hence there will not be repetition of the detailed description here.

[0063] A characteristic configuration of the embodiment shown in figure 2 is provided with an insulator part 21a on the inner side of the ceiling part of the upper vessel 12a, and the electrodes 20 are arranged on the underside of said insulator part 21a. There are no holes 20a formed in the electrodes 20 so that it has the shape of a single plate. The plasma-generating space 15 is formed by the electrodes 20 and partition plate 14, which forms a parallel plate type electrode configuration.

[0064] The rest of the configuration is essentially the same as the configuration of the first embodiment.

[0065] A general description will be given of the film-deposition method which uses a cleaning method according to the invention, with respect to the CVD system configured as above. A glass substrate 11 is transferred to the interior of

the vacuum vessel 12 by means of a transferring robot (not shown in the figure), and is placed on the substrate-holding mechanism 17. The interior of the vacuum vessel 12 is exhausted by means of the exhaust device 13, and a prescribed vacuum state is maintained by reducing the pressure. Next, oxygen gas is fed into the plasma-generating space 15 of the vacuum vessel 12 through the oxygen gas feed pipe 23a.

[0066] A material in gas form, for example silane, is fed into the interior space 24 of the partition plate 14 through the feed pipe 28. The silane is fed firstly into the upper side part of the interior space 24, it is homogenized by the homogenizing plates 27 and moved to the lower side part, and next fed directly into the film-deposition processing space 16 through the diffusion holes 26, i.e., without coming into contact with the plasma. The substrate-holding mechanism 17 which is installed in the film-deposition processing space 16 is maintained in advance at a prescribed temperature because electricity is transmitted to the heater 18.

[0067] In the state mentioned above, high-frequency electric power is supplied to the electrodes 20 via the electric power feed rod 29. Electric discharge is produced by means of this high-frequency electric power, and oxygen plasma 19 is generated in the vicinity of the electrode 20 inside the plasma-generating space 15. By virtue of the fact that oxygen plasma 19 is generated, radicals (excited active species) which are neutral excited species are generated, silicon oxides are deposited on the surface of the substrate 11.

[0068] Next, a description will be given of a cleaning method according to the invention which is applied to the above-mentioned CVD system, for the case in which NF₃ gas is used as the cleaning gas.

[0069] Cleaning is performed periodically at every preset time interval or when a preset number of substrates are processed. For cleaning, after stopping of a material gas feeding such as silane gas, and by replacing the oxygen gas which is introduced into the plasma generation space at film deposition period with fluorine

gas. System configuration is not different by almost same even if used cleaning gas differs.

[0070] A partition plate 14 which is formed from electrically conductive material is placed at ground potential, NF₃ gas as cleaning gas is fed into the plasma-generating space 15, and fluorine radicals are generated inside the plasma-generating space 15 by supplying high-frequency electric power to the electrodes 20. The fluorine radicals which are generated are fed into the film-deposition processing space 16 through the plurality of through-holes 25 in the partition plate 14, and by this means the interior of the film-deposition processing space 16 is cleaned.

[0071] It is also possible to admix an inert gas, such as Ar gas, or oxygen gas to the cleaning gas (NF₃) in order to improve the cleaning speed.

[0072] In addition, oxygen can be used as the cleaning gas in cases where carbonates are deposited.

[0073] In addition, in cases where fluoride gas is used as the cleaning gas, the adsorption of fluorine onto the inner circumferential face of the through-holes 25 and the surface of the partition plate must be prevented, and depending on the type of fluoride gas used as the cleaning gas, it is desirable to heat the partition plate 14 to 200°C or more by means of the heater 30, when fluorocarbon gas such as CF₄, C₂F₆, C₃F₈ or nitrogen fluoride gas such as NF₃ are used, for example. Or, it is desirable to heat the substrate 14 to a temperature of 100°C or more in cases when using fluoro sulfur gas such as SF₆, while carrying out said cleaning process.

[0074] An example of specific setting values for the cleaning method for a silicon oxide film according to the present invention is given below.

[0075] Ar gas for speeding up the dissociation of cleaning gas was admixed to the cleaning gas (NF₃) with an Ar gas flow rate of 100 cm³/min (0.18 g/min) under standard conditions with a power applied to the 60 MHz high-frequency electrodes 20 of 2 kW, and a mass flow rate, under standard conditions, of the NF₃ cleaning gas of 200 cm³/min (0.63 g/min). The pressure of the film-

deposition processing space 16 was 16 Pa. The speed with which the silicon oxide in the film-deposition processing space 16 was removed, in other words the cleaning speed, was 30 to 40 nm/min.

[0076] As has been made clear in the description above, by means of the present invention, it is possible to provide an optimum cleaning method for a system which can deposit silicon oxide film and the like on a large-area substrate using a material in gas form such as silane by means of plasma CVD, in which CVD system, for example, the interior of the vacuum vessel is divided into a plasma-generating space and a film-deposition processing space by the position of an electrically conductive partition plate with a plurality of through-holes or diffusion holes formed therein, and active species are generated in the plasma-generating space and are fed into the film-deposition processing space through a plurality of holes in said partition plate.

[0077] With the cleaning method according to the present invention, by performing optimum cleaning after film-deposition, the generation of particles can be reduced and the CVD system of the present form configuration can be used efficiently in the manufacture of large-area substrates without interpret due to exposing the interior of the depositing chamber to the atmospheric ambient for cleaning, and yet there is no product contamination due to fluorines caused by the cleaning gas, and it is possible to operate continuously in a stabilized fashion without interpret due to exposing the interior of the depositing chamber to the atmospheric ambient for cleaning, which results in a high yield.

[0078] Although only preferred embodiments are specifically illustrated and described herein, it will be appreciated that many modifications and variations of the present invention are possible in light of the above teachings and within the purview of the appended claims without departing from the spirit and intended scope of the invention.